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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Manabu NAKAMURA et al.

Serial Number: 10/659,748 Application Division

Filed: September 11, 2003 Customer No.:38834

For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

## REQUEST FOR CORRECTED FILING RECEIPT

Date: April 20, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please supply the undersigned attorney with a corrected filing receipt for the above-identified application. The undersigned also respectfully requests that the Patent and Trademark Office records be amended to reflect the correction.

In reviewing the Official Filing Receipt, we noted an error in the Fifth Applicant's Address. The Fifth Applicant's Address should read —<u>Aizuwakamatsu</u>, <u>JAPAN</u>;—. A copy of **Declaration** is enclosed which indicates the correct information. We are enclosing a copy of the filing receipt with the corrections highlighted.

In the event that any fees are required in connection with this paper, please charge our Deposit Account No. <u>50-2866</u>.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

Stephen G. Adrien Attorney for Applicants Reg. No. 32,878

Atty. Docket No. **031140** Suite 700 1250 Connecticut Avenue N.W. Washington, D.C.20036 (202)822-1100

SGA/my

Enclosures: Official Filing Receipt; Copy of Declaration



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UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P. O. States Alexandia, Vignus 22313-1450

FILING OR 371 APPL NO. ART UNIT FIL FEE REC'D ATTY.DOCKET NO **DRAWINGS** TOT CLMS IND CLMS (c) DATE 10/659.748 09/11/2003 2812 900 031140 18

**CONFIRMATION NO. 3468** 

**UPDATED FILING RECEIPT** 

OC000000011723543\*

38834 WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP 1250 CONNECTICUT AVENUE, NW SUITE 700 WASHINGTON, DC 20036

Date Mailed: 01/21/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Manabu Nakamura, Aizuwakamatsu, JAPAN: Hiroyuki Nansei, Aizuwakamatsu, JAPAN: Kentaro Sera, Aizuwakamatsu, JAPAN; Masahiko Higashi, Aizuwakamatsu, JAPAN: Yukihiro Utsuno, Alizuwakatsu, JAPAN; Hideo Takagi, Aizuwakamatsu JAPAN: Tatsuya Kajita, Aizuwakamatsu JAPAN;

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izuwakamatsu --Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-273625 09/19/2002

If Required, Foreign Filing License Granted: 12/03/2003

Projected Publication Date: 04/29/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing semiconductor device

**Preliminary Class** 

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